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System Editor Simulator Ver	ifier	
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🔄 train-gate	* For more details about this example, see	
 Global declarations 	* "Automatic Verification of Real-Time Communicating Systems by Constraint Solving",	
⊞∽‰, Train ⊞∽‰, Gate	* by Wang Yi, Paul Pettersson and Mats Daniels. In Proceedings of the 7th International * Conference on Formal Description Techniques, pages 223-238, North-Holland. 1994.	
⊡ Si IntQueue	 Conference on Formal Description Techniques, pages */ 	8 223-238, North-Holland. 1994.
 Process assignments 	-/	
System definition	const N 5; // # trains + 1	
	<pre>int[0,N] el;</pre>	Constants
	chan appr, stop, go, leave;	Bounded integers
	chan empty, notempty, hd, add, rem;	
train-gate Global declarations	clock x;	Channels
		Clocks
Declarations		
🔄 train-gate	<pre>int[0,N] list[N], len, i;</pre>	Arrays
Global declarations □ [∞] Train		
Declarations		Tompletee
±-S3 Gate		Templates
⊡ 🖏 IntQueue		Processes
Declarations		Syctome
 Process assignments System definition 	<pre>Train1:=Train(el, 1);</pre>	Systems
	Train2:=Train(el, 2);	-,
	Train3:=Train(el, 3); Train4:=Train(el, 4);	
÷ 90 1.00	<pre>lrain4:=lrain(e1, 4); lueue:=IntQueue(e1);</pre>	
E-S IntQueue		ЩГ
 Process assignments 	Trainl, Train2, Train3, Train4,	
 Process assignments System definition 	Gate, Queue;	































































