Code Generation from Extended Finite State Machines



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EXTENDED FINITE STATE MACHINES



- A Keyboard Example
- Formal Definitions
- Variants

















State Machine Coding Schemes

- State Machine Interface
- A Running Example
- Coding Schemes
 - o Nested Switch Statement
 - o State Table
 - o Object-oriented State Design Pattern
 - o Multiple-threaded Implementation
- Tradeoffs between EFSM Implementations







The Nested Switch Statement: Example

<pre>enum Signal { CHAR_SIG, STAR_SIG, SLASH_SIG };</pre>	<pre>// enumeration for CParser signals</pre>	
<pre>enum State { CODE, SLASH, COMMENT, STAR };</pre>	<pre>// enumeration for CParser states</pre>	
<pre>class CParser1 { private: State myState; long myCommentCtr; /* */ public: State myState state state myState statemyState state statemyStatemyStatemyStatemyStatemyS</pre>	// the scalar state-variable tate machine interface other CParserl attributes	
<pre>void init() { myCommentCtr = void dispatch(unsigned const void tran(State target) { myS long getCommentCtr() const { };</pre>	0; tran(CODE); } // default transiton sig); tate = target; } return myCommentCtr; }	
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Multiple-threaded Implementation

Approach I

o Each EFSM is implemented inside one thread.

- o Threads run simultaneously, scheduled in round-robin.
- o EFSMs share variables in the process.

Advantage

- o Straightforward transformation from model.
- o EFSM communication easily implemented with thread messages.

Disadvantage

- o In some situations, no ready thread support in specific platform.
- Related analysis (progressiveness if semaphores are used, timing properties, etc) may be difficult.

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Multiple-threaded Implementation Approach II o Event detectors are implemented in threads. o Transition actions are implemented in functions. o Location information is stored with a variable. o When event detector threads detects, calls corresponding functions and switching locations. Advantage o Easy adaption to model changes. Disadvantage o In some situations, no ready thread support in specific platform. o Code may be unstructured/unreadable. CIS 541 Spring '10 41

Multiple-threaded Implementation: Example (Approach II) void *trans3(void *ptr) { A transition logic is written inside a int t; function while(1) { sem wait(&Sense); • Wait for semaphore for triggering if(current==WAIT_VRP && TRUE) { (WAIT_VRP) and guard (TRUE). current=ST IDLE; sem_post(&ST_IDLE); o Execute updates. } • Print out timer value (for debugging) } Reset timer value } Change state int main(int argc, char *argv[]) { pthread t thread1, thread2, thread3; • All threads initialized and run in main // ... initilization code ... pthread_create(&thread1, NULL, trans1, NULL); pthread create(&thread2, NULL, trans2, NULL); pthread_create(&thread3, NULL, trans3, NULL); pthread join(thread1, NULL); return 0; } CIS 541 Spring '10 42



THE EFSM TOOLSET

The EFSM Toolset

- Introduction
- The EFSM Language
- Checking for Non-determinism and Totality
- Translations to Other Languages
- Test Generation from EFSMs
- Script Generation
- Code Generation
- Simulation





























